

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	460	712/1.ccls.	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:15
L2	97	712/18.ccls.	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:15
L3	126	712/25.ccls.	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:15
L4	323	712/34.ccls.	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:15
L5	256	712/35.ccls.	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:15
L6	157	712/201.ccls.	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:15
L7	1303	1 or 2 or 3 or 4 or 5 or 6	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:16
L9	1	pipelined adj configurable adj array	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:16
L10	454	lookup near3 cell\$1	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:17
L11	64	fpga and 10	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:17
L12	1	vliw and 11	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:17
L13	1	risc and 12	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:18
L14	1151	embed\$4 near3 (fpga or asic)	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:18
L15	3515	vliw	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:18
L16	2	15 with pld	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:18
L17	53524	microcontroller	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:19
L18	2611	very adj long adj instruction	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:19
L19	545	17 and 18	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:19
L20	8	elaboration adj mode	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:19
L21	1923	(fpga or asic) near3 cell\$1	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:20
L22	59	table with 21	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:20

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L23	805	vliw adj processor	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:20
L24	27	(fpga or asic) with 23	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:20
L25	906	emulat\$3 near3 (microprocessor or microcomputer)	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:21
L26	2	array near3 25	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:21
L27	1468	(fpga or asic) near3 register\$1	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:22
L28	593	cell\$1 and 27	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:22
L29	311	embed\$4 and 28	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:23
L30	65369	cell\$1 near3 array	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:23
L31	51	29 and 30	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:23
L32	143	array with vliw	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:23
L33	51	31 and (fpga or asic)	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:23
L34	1	pico adj array	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:25
L35	970	pipelin\$3 near3 (row\$1 or column\$1)	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:26
L36	28	(activat\$3 or enabl\$3)near3 35	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:27
L37	0	7 and 36	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:27
L38	1540	control\$3 near3 activat\$3 near3 (row\$1 or column\$1)	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:29
L39	2	21 and 38	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:29
L40	51198	(data adj flow) or (data adj driven)	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:30
L41	136	35 and 40	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:31
L42	3	38 and 41	US-PGPUB; USPAT	OR	OFF	2006/11/30 10:31

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L1	288	(activat\$3 or deactivat\$3)near3 stripe\$1	US-PGPUB; USPAT	OR	OFF	2006/11/30 14:40
L2	68480	fpga or asic	US-PGPUB; USPAT	OR	OFF	2006/11/30 13:17
L3	0	1 with 2	US-PGPUB; USPAT	OR	OFF	2006/11/30 13:17
L4	2	1 and 2	US-PGPUB; USPAT	OR	OFF	2006/11/30 13:17
L5	1	("6062773").PN.	USPAT; USOCR	OR	OFF	2006/11/30 13:18
L6	1	("6052773").PN.	USPAT; USOCR	OR	OFF	2006/11/30 13:20
L7	10190	(activat\$3 or deactivat\$3)near3 row\$1	US-PGPUB; USPAT	OR	OFF	2006/11/30 13:20
L8	48336	dataflow or data adj flow	US-PGPUB; USPAT	OR	OFF	2006/11/30 13:20
L9	2	7 with 8	US-PGPUB; USPAT	OR	OFF	2006/11/30 13:21
L10	285	7 and 8	US-PGPUB; USPAT	OR	OFF	2006/11/30 14:38
L11	46	data adj path adj cell\$1	US-PGPUB; USPAT	OR	OFF	2006/11/30 14:38
L12	984942	row\$1 or column\$1	US-PGPUB; USPAT	OR	OFF	2006/11/30 14:39
L13	6	11 with 12	US-PGPUB; USPAT	OR	OFF	2006/11/30 14:39
L14	0	activat\$3 with 13	US-PGPUB; USPAT	OR	OFF	2006/11/30 14:39
L15	188026	stripe\$1 or row\$1 or column\$1	EPO; JPO	OR	OFF	2006/11/30 14:40
L16	1082	data adj path	EPO; JPO	OR	OFF	2006/11/30 14:40
L17	28	15 and 16	EPO; JPO	OR	OFF	2006/11/30 14:40
L19	123257	(activat\$3 or deactivat\$3)	EPO; JPO	OR	OFF	2006/11/30 14:41
L20	0	17 and 19	EPO; JPO	OR	OFF	2006/11/30 14:41